

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.
ARC.003A

APPLICATION NO.
09/801,241

INFORMATION DISCLOSURE STATEMENT
BY APPLICANT

APPLICANT
David Latta

FILING DATE
March 7, 2001

GROUP
2181

(USE SEVERAL SHEETS IF NECESSARY)

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PAGE 1 OF 1

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
AKM	1	5,247,637	08/21/93	Leedom, et al.	1	1	
	2	5,394,515	02/28/95	Lentz, et al.	1	1	
	3	5,481,707	01/02/98	Murphy, et al.	1	1	
	4	5,537,604	07/16/96	Baum, et al.	1	1	
	5	5,559,951	09/24/96	Lentz, et al.	1	1	
	6	5,564,117	10/08/96	Lentz, et al.	1	1	
	7	5,636,364	06/03/97	Emma, et al.	1	1	
	8	5,960,468	09/28/99	Edward J. Paluch	1	1	
	9	5,968,114	10/19/99	Wentka, et al.	1	1	
	10	5,987,574	11/16/99	Edward J. Paluch	1	1	
	11	6,038,646	03/14/00	Robert F. Sproull	1	1	
	12	6,091,431	07/18/00	Saxena, et al.	1	1	
	13	6,092,167	07/18/00	Cheung, et al.	1	1	
	14	6,141,739	10/31/00	Provence, et al.	1	1	
	15	6,182,192	01/30/01	Fabrizio Rovati	1	1	
	16	6,240,492	05/29/01	Foster, et al.	1	1	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO

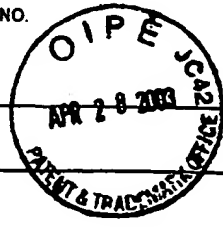
OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

EXAMINER INITIAL		
AKM	17	Application Serial No. 09/418,663 entitled "Method and Apparatus for Managing the Configuration and Functionality of a Semiconductor Design," filed October 14, 1999 - Attorney Docket No. ARC.001A

EXAMINER *Barbara K. Mason* DATE CONSIDERED *12-8-2003*

*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. ARC.003A	APPLICATION NO. 08/801,241
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)		APPLICANT David Latta	GROUP 2181
		FILING DATE March 7, 2001	



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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
DKM	1	EP 0 479 390 A2	4/8/92	EP			

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)
DKM	2 The CoreConnect Bus Architecture - XP-002204519 by International Business Machines Corporation, 1999.
	3 Embedded DRAM for a Reconfigurable Array - XP-000894790 by Symposium on VLSI Circuits Digest of Technical Papers
	4 A Scalable Architecture for System-On-Chip Interconnections - XP-002204520 by Pierre Guerrier and Alain Greiner dated October 22, 1999.
↓	5 A Reconfigurable Bus IP for Modular Function Integration - XP-002204520 by Neil S. Stollen, Ph.D., P.E. at Infinite Technology Corp. dated March 22, 1999.
EXAMINER	DATE CONSIDERED
Norman A. Mason	12-8-2003

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